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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Schroeder et al.

Examiner: Nadav, O.

Application No.: 09/389,826

Art Unit: 2811

Filed: 09/03/1999

For: SEMICONDUCTOR DEVICE

TECHNOLOGY CENTER 2800

JUN 17 2002

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#18/E
Wheeler
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Box Non-Fee Amendment
Commissioner for Patents
Washington D.C. 20231

AMENDMENT

Sir:

In response to the Office Action of March 7, 2002, Applicants hereby respond as follows:

IN THE CLAIMS

Please amend claims 7 and 9 to appear as set forth below. A separate markup sheet accompanies this amendment. For clarity purposes, all claims are shown below.

1. (Pending) A semiconductor device having a semiconductor body which on a surface comprises an integrated circuit containing protection means for protection against electrostatic discharge (ESD), the means being a compound element of an SCR and at least two gated diodes, the protection means being provided in a surface area of a first conductivity type having a single well of a second, opposite, conductivity type,

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